

FIG. 2

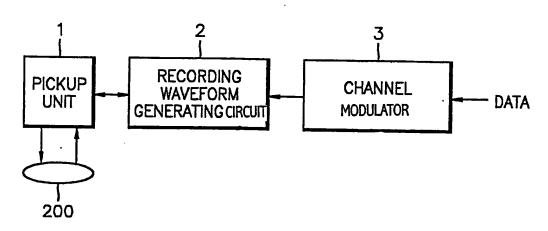
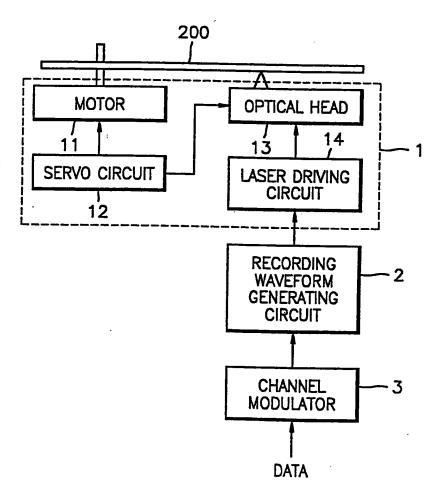
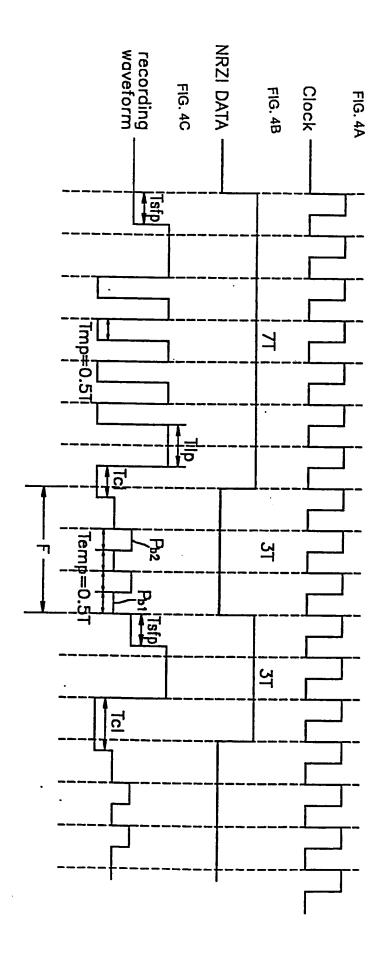
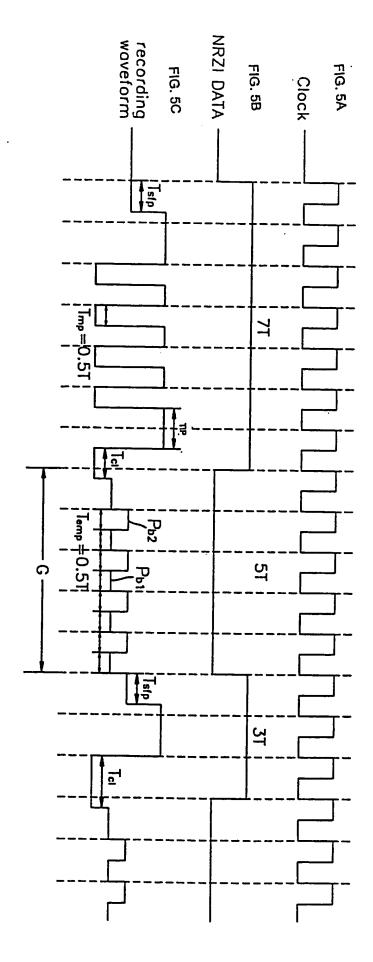


FIG. 3







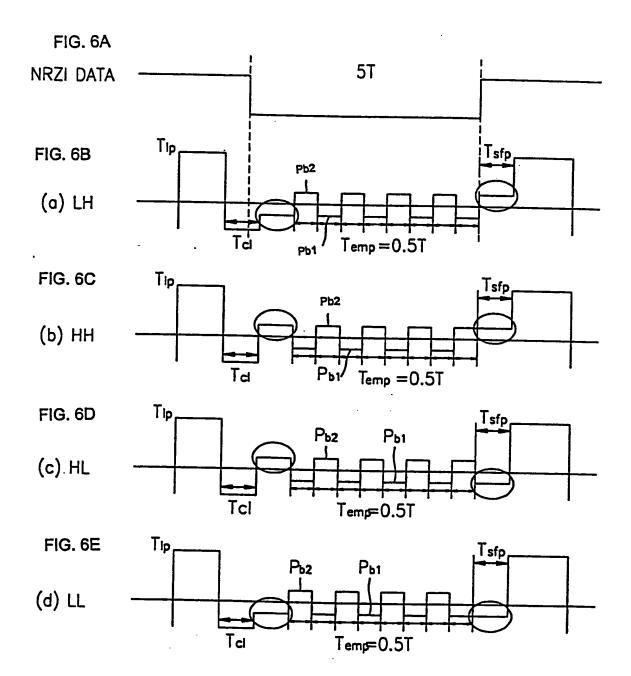
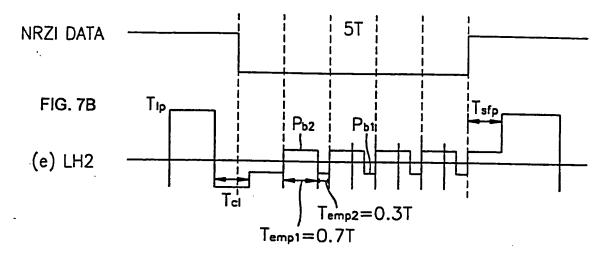
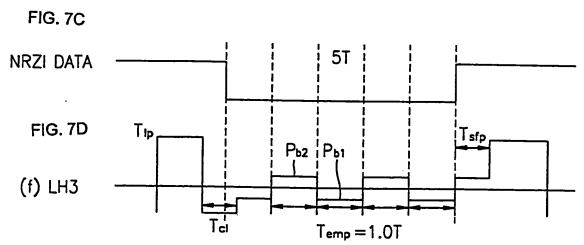


FIG. 7A





DEST AVAILABLE COPY

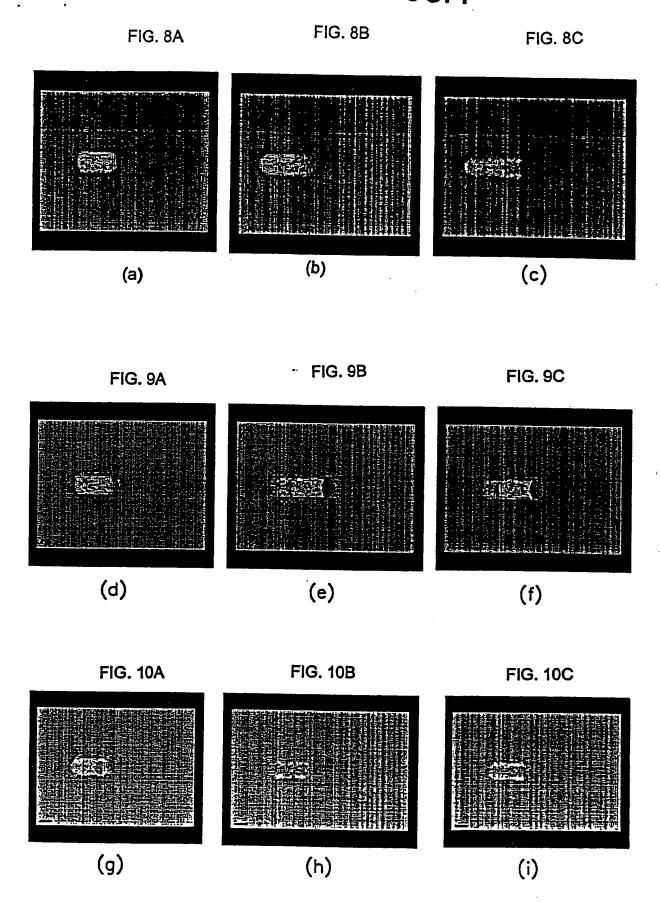


FIG. 11A (PRIOR ART)

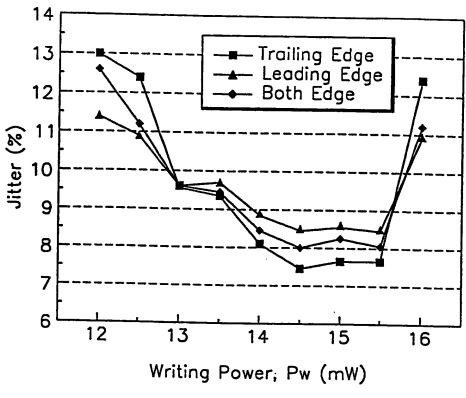
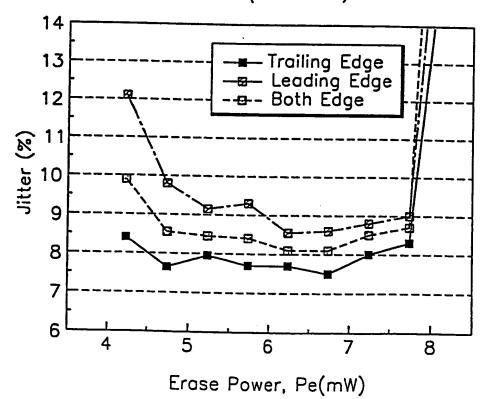
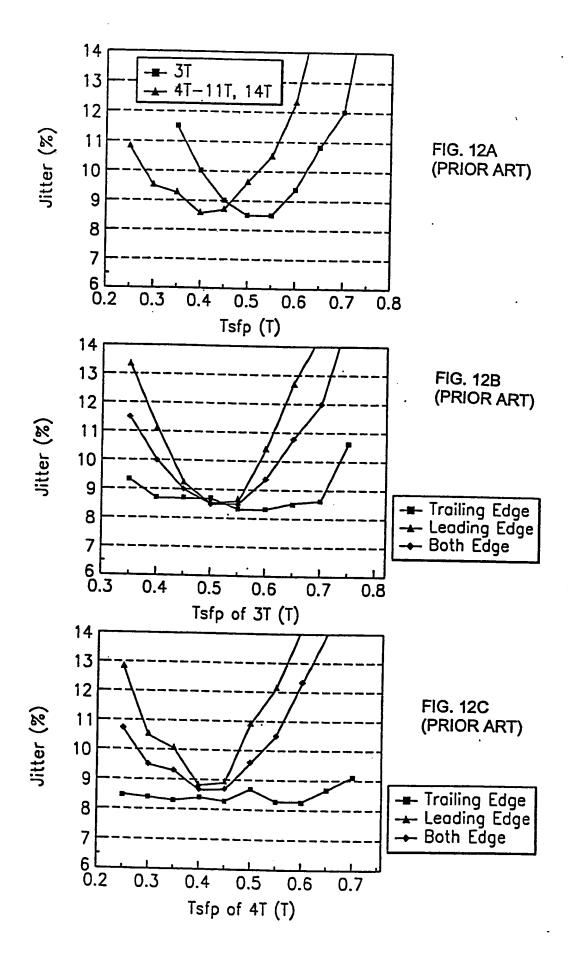


FIG. 11B (PRIOR ART)





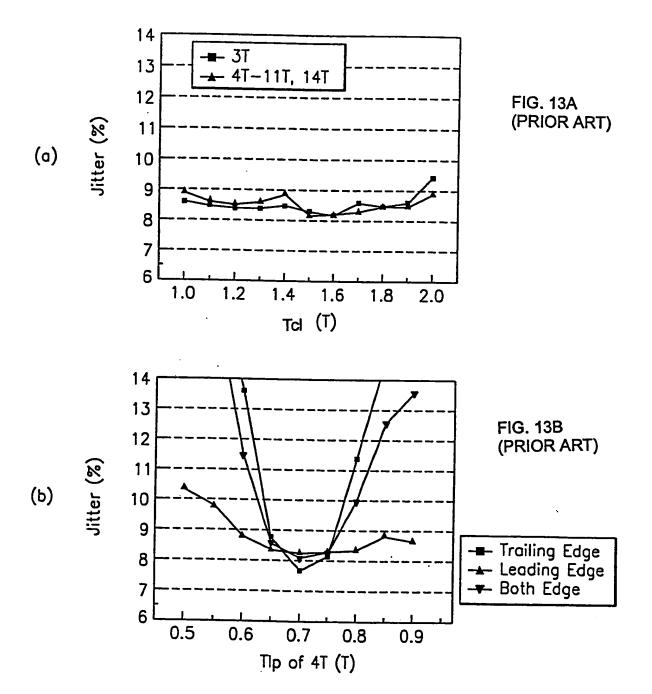


FIG. 14A

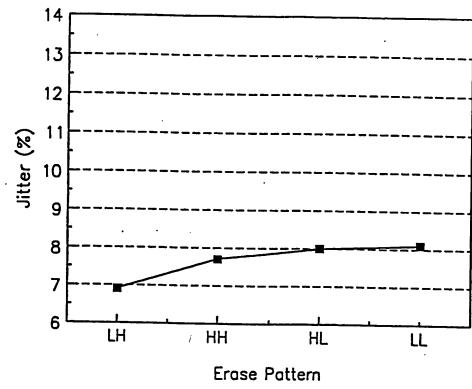


FIG. 14B

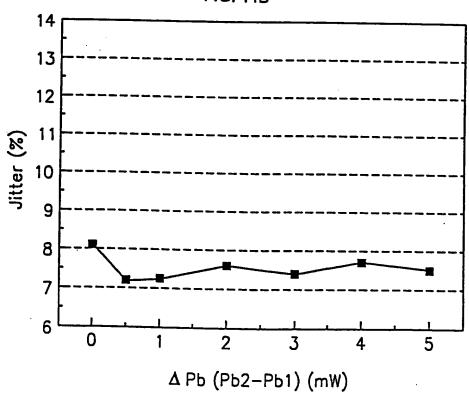
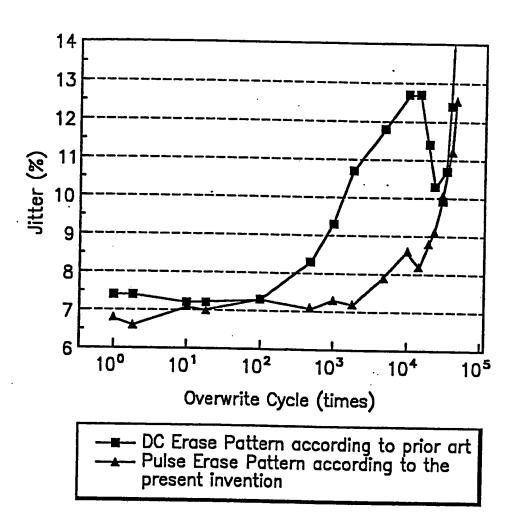


FIG. 15



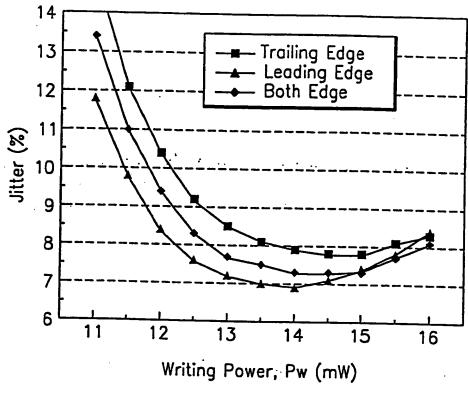
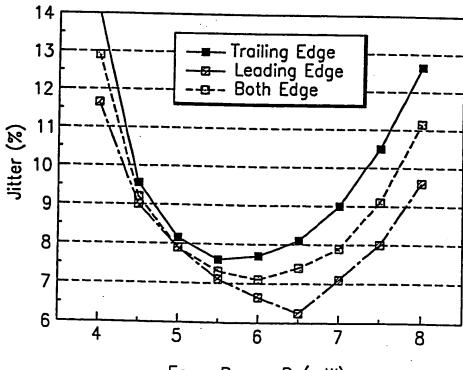
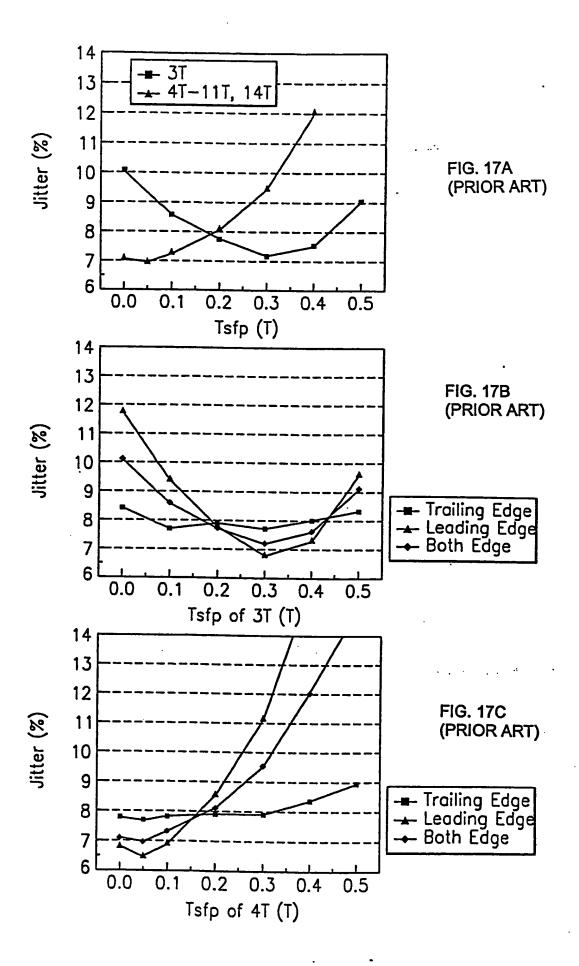
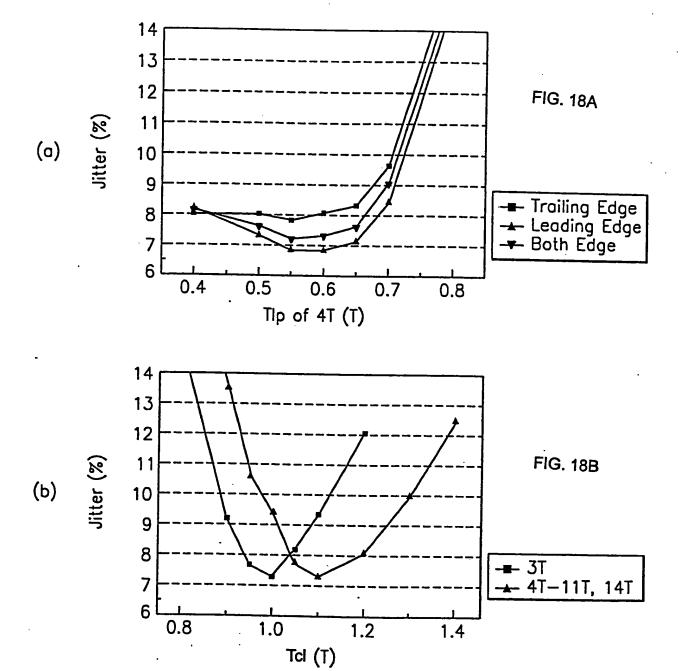


FIG. 16B (PRIOR ART)

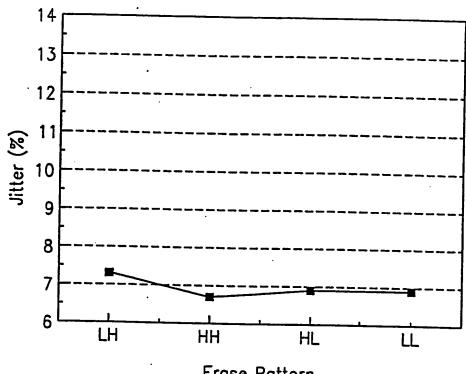


Erase Power, Pe(mW)

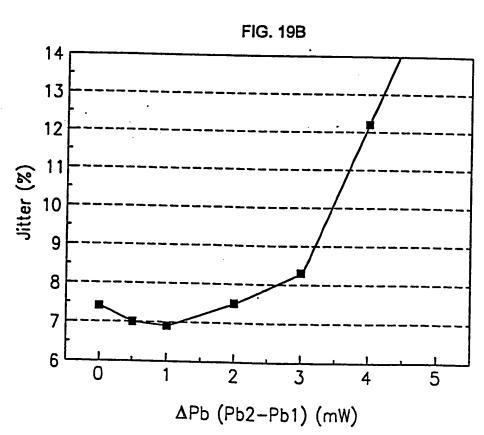


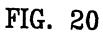


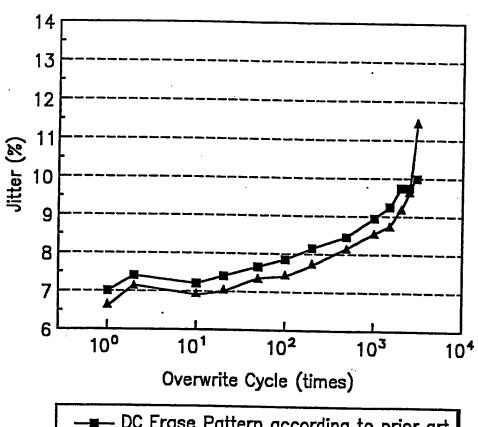




Erase Pattern







→ DC Erase Pattern according to prior art

→ Pulse Erase Pattern according to the present invention

FIG. 21

